

PATENT APPLICATION OF

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For

**Texture on Substrate and a Method for Localizing and Minimizing Effects
of Lattice Mismatch**

ABSTRACT

Lattice mismatch is a critical issue for semiconductor devices including nitride LED. Texturing a substrate, texturing an epitaxial layer, and a method are disclosed in the present invention for localizing and minimizing the effects of lattice mismatch. Texturing may be applied for more than once on the same epitaxial wafer. Thus different epitaxial layers comprising different active layers may be grown on the same wafer and, therefore, the semiconductor device may emit light of a combination of different wavelengths.